DAC016N170PY2

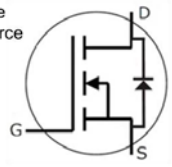
Silicon Carbide Enhancement Mode MOSFET

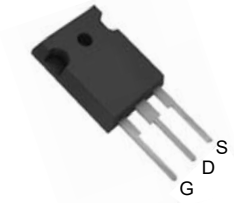
V_{DSS}	1700V
$I_{D(@25℃)}$	138A
$R_{DS(ON)\ typ.}$	16mΩ

Features

- High blocking voltage with low Rds(on)
- High frequency operation with low Capacitance
- Simple to drive and easy to parallel
- Robust body diode with low Qrr
- 100% Avalanche Tested

D : Drain
G : Gate
S : Source





TO-247-3L

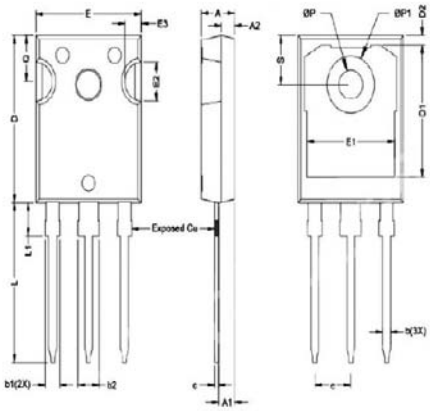
Benefits

- Superior robustness and system reliability
- Higher system efficiency
- Easier paralleling without thermal runaway
- Capable of high temperature application
- Faster and more efficient switching

Applications

- EV motor drives
- EV/HEV charging station
- Energy storage and Battery charging
- High voltage DC-DC converters
- Solar / Wind Inverters
- UPS and PFC

Package Dimensions



Absolute Maximum Ratings

(Tc = 25℃ unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Drain-Source Voltage	V_{GS}=0V, I_D=100μA	V_{DS}	1700	V
Gate-Source Voltage (dynamic)	AC (f>1 Hz, duty cycle<1%, pulse width<200ns)	V_{GS}	-10/+25	V
Gate-Source Voltage (static)		$V_{GS(op)}$	-4/+18	V
Drain Current-Continuous	V_{GS}=18V@ T_C=25°C V_{GS}=18V@ T_C=100°C	I_D	138 100	A
Pulse Drain Current		$I_{D,pulse}$	275	A
Power Dissipation		P_D	575	W
Storage Temperature Range		T_{STG}	-55 to +175	°C
Operating Junction Temperature Range		T_J	-55 to +175	°C
Soldering Temperature		T_L	260	°C
Avalanche Capability, single pulse *	V_{DD}=100V, V_{GS}=10V, L=1mH	I_{AV}	88	A
Avalanche Capability, single pulse**	V_{DD}=100V, V_{GS}=10V, L=1mH	E_{AV}	3700	mJ

* 100% tested in 60% rating
** 100% tested in 36% rating

Unit : mm

DIM	MILIMETERS		
	MIN	TYP.	MAX
A	4.82	5.02	5.22
A1	2.21	2.41	2.61
A2	1.8	2	2.2
b	0.95	1.2	1.45
b1	1.95	2.2	2.45
b2	2.95	3.2	3.45
c	0.35	0.6	0.85
D	20.75	20.95	21.15
D1	16.3	16.55	16.8
D2	0.99	1.19	1.39
E	15.74	15.94	16.14
E1	13.01	13.26	13.51
E2	4.71	4.91	5.11
E3	2.26	2.46	2.66
e	5.44 BSC.		
L	19.82	20.07	20.32
L1	3.94	4.19	4.44
P	3.41	3.61	3.81
P1	6.94	7.19	7.44
Q	5.59	5.79	5.99
S	5.97	6.17	6.37

Electrical Characteristics @ T_C =25°C (unless otherwise specified)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
OFF Characteristics							
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS} =0V，I_D =0.1mA		1700	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} =1700V V_{GS} =0V	T_J =25℃	-	1	100	μA
			T_J =175℃	-	10	-	
Gate-Source Leakage Current	I_{GSS}	V_{GS} =18V，V_{DS} =0V		-	5	100	nA
		V_{GS} =-4V，V_{DS} =0V		-100	-5	-	
ON Characteristics							
Gate Threshold Voltage ***	$V_{GS(th)}$	V_{DS} = V_{GS}，I_D =30mA	T_J =25℃	2.5	3.1	4.2	V
			T_J =175℃	-	2.4	-	
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS} =18V，I_D =75A	T_J =25℃	-	16	22	mΩ
			T_J =175℃	-	36	-	
Transconductance	g_{fs}	V_{DS} =20V，I_D =75A	T_J =25℃	-	60	-	S
			T_J =175℃	-	58	-	
Internal Gate Resistance	$R_{G(int.)}$	f =1MHz，I_D =0A		-	0.95	-	Ω
Dynamic Characteristics							
Input Capacitance	C_{iss}	V_{DS} =1200V V_{GS} =0V f =100kHz V_{AC} =25mV		-	6400	-	pF
Output Capacitance	C_{oss}			-	180	-	
Reverse Transfer Capacitance	C_{rss}			-	20	-	
Coss Stored Energy	E_{oss}			-	160	-	μJ
Turn-On Switching Energy	E_{on}	V_{DS} =850V，V_{GS} =-4/+18V I_D =75A，$R_{G(ext)}$ =2.2Ω L=370μH		-	540	-	μJ
Turn-Off Switching Energy	E_{off}			-	1770	-	
Switching Characteristics							
Turn-On Delay Time	$t_{d(on)}$	V_{DS} =850V，V_{GS} =-4/+18V I_D =75A，$R_{G(ext)}$ =2.2Ω L=370μH		-	56	-	ns
Rise Time	t_r			-	39	-	
Turn-Off Delay Time	$t_{d(off)}$			-	95	-	
Fall Time	t_f			-	10	-	
Total Gate Charge	Q_g	V_{DS} =1200V V_{GS} =-4/+18V I_D =75A		-	320	-	nC
Gate to Source Charge	Q_{gs}			-	88	-	
Gate to Drain Charge	Q_{gd}			-	130	-	
Body Diode Characteristics							
Inverse Diode Forward Voltage	V_{SD}	V_{GS}=-4V，I_{SD}=50A	T_J =25℃	-	4.2	-	V
Inverse Diode Forward Voltage			T_J =175℃	-	3.7	-	V
Continuous Diode Forward Current	I_S	V_{GS}=-4V，T_J =25°C		-	120	-	A
Reverse Recovery Time	T_{rr}	I_{SD}=75A，V_{GS}=-4V V_R=1200V dif/dt=1704 A/μs		-	29	-	ns
Reverse Recovery Charge	Q_{rr}			-	460	-	nC
Peak Reverse Recovery Current	I_{rrm}			-	30	-	A
Thermal Resistance							
Thermal Resistance, Junction-to-Case	$R\theta_{JC}$			-	0.15	0.18	°C/W

*** Turn-off with -2V to -5V gate bias is recommended

Typical Performance

Fig 1. Output Characteristics, T_J = -40 °C

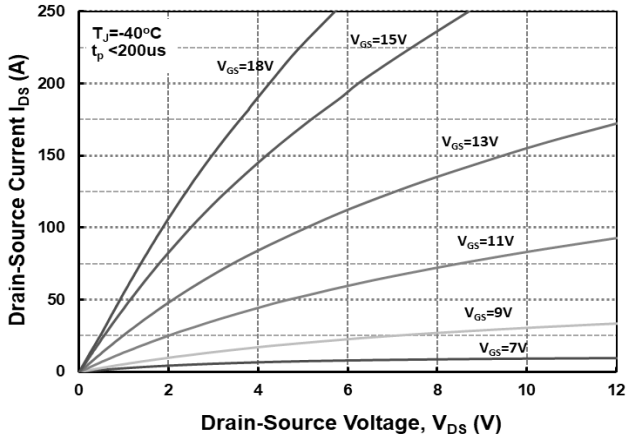


Fig 2. Output Characteristics, T_J = 25°C

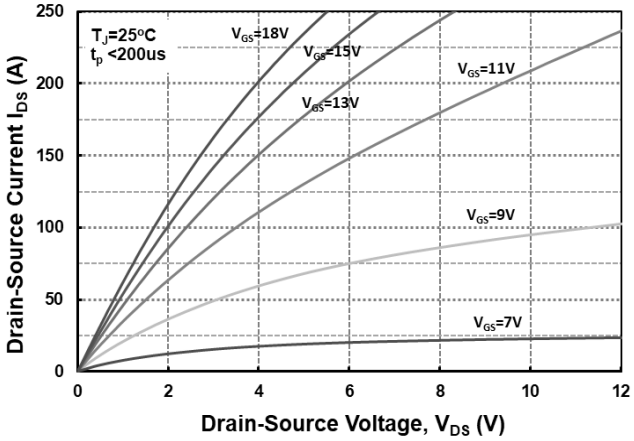


Fig 3. Output Characteristics, T_J = 175°C

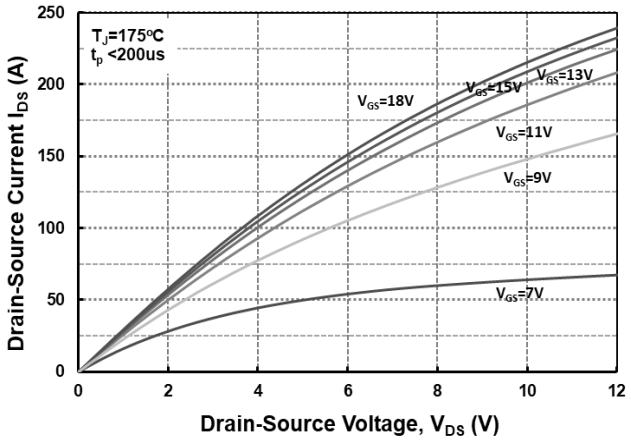


Fig 4. Normalized On-Resistance vs. Temperature

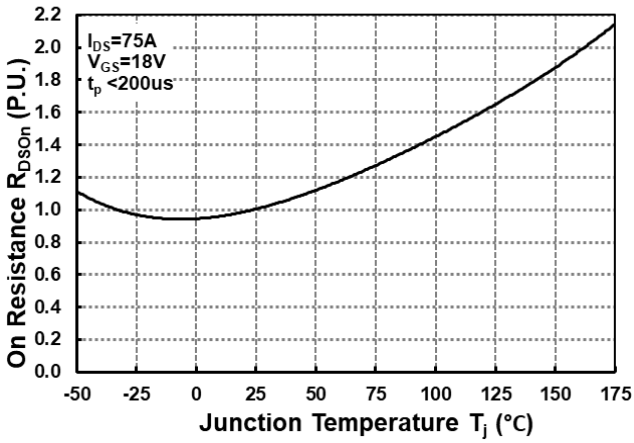


Fig 5. On-Resistance vs. Drain Current for Various Temperatures

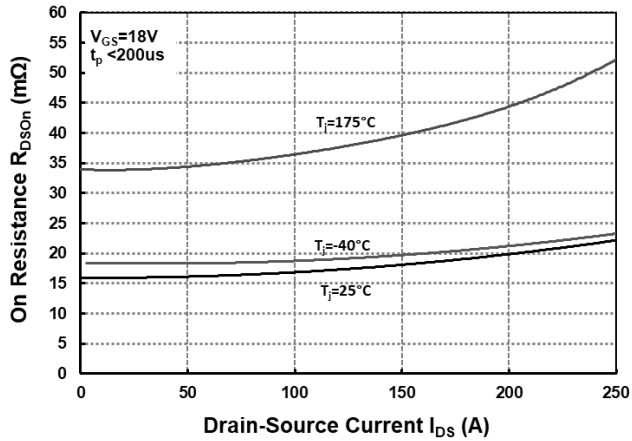
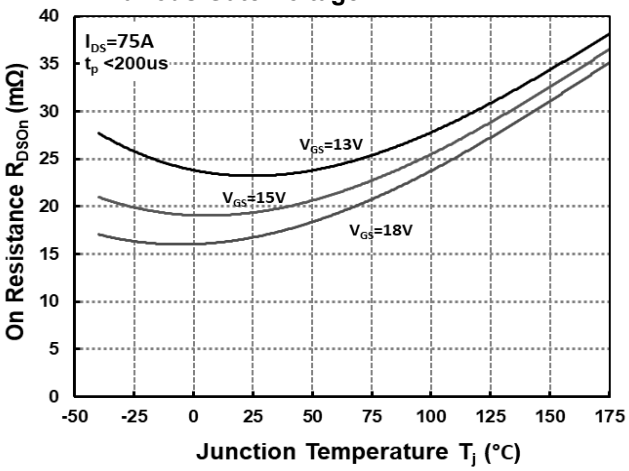


Fig 6. On-Resistance vs. Temperature for Various Gate Voltage



Typical Performance

Fig 7. Transfer Characteristic for Various Junction Temperatures

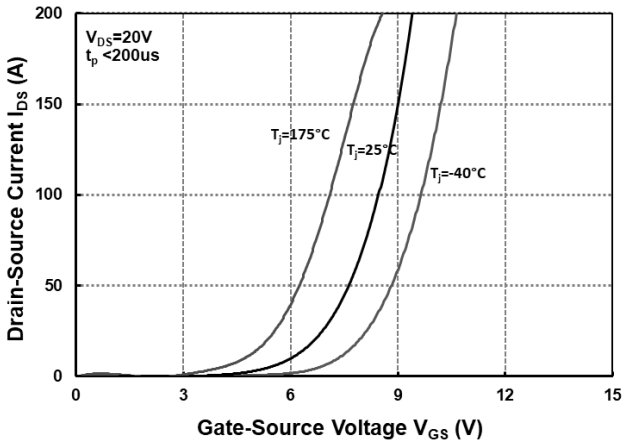


Fig 8. Body Diode Characteristics @ -40°C

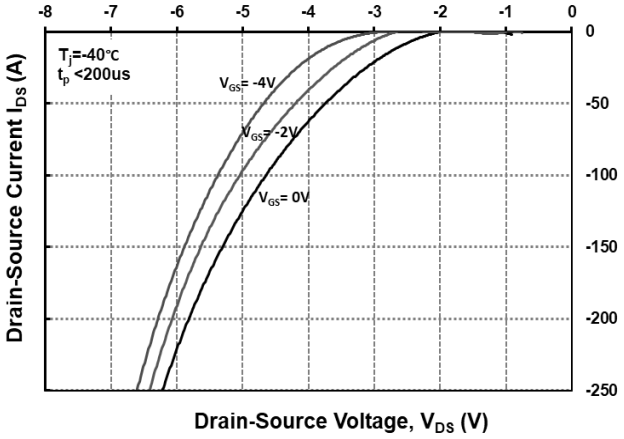


Fig 9. Body Diode Characterisics @ 25°C

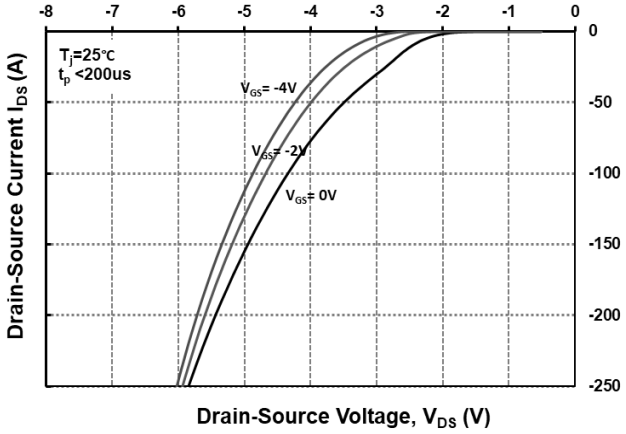


Fig 10. Body Diode Characteristics @ 175°C

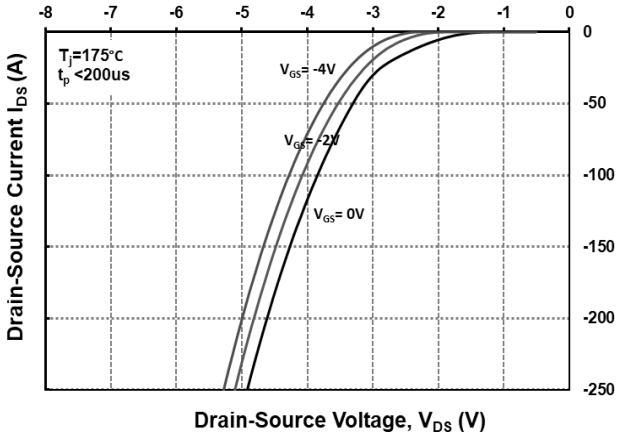


Fig 11. Threshold Voltage vs. Temperature

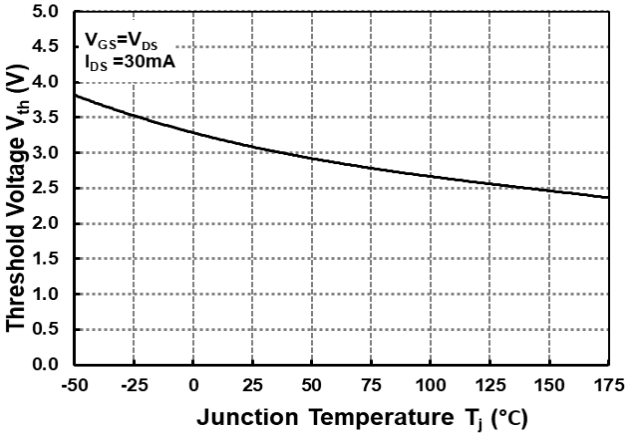
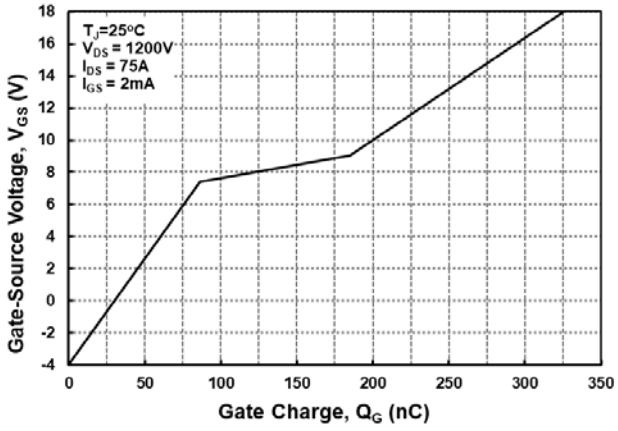


Fig 12. Gate Charge Characteristics



Typical Performance

Fig 13. 3rd Quadrant Characteristics @ -40°C

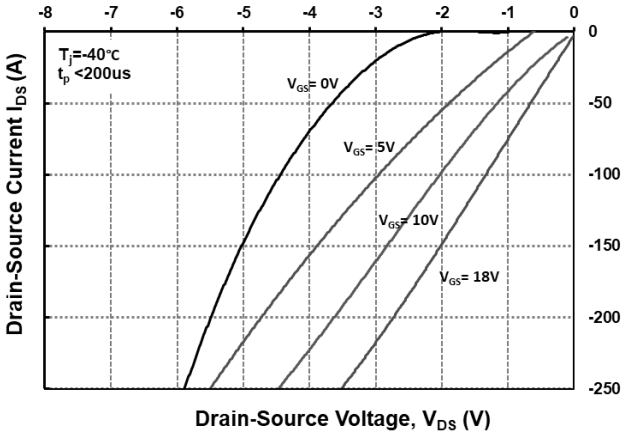


Fig 14. 3rd Quadrant Characteristics @ 25°C

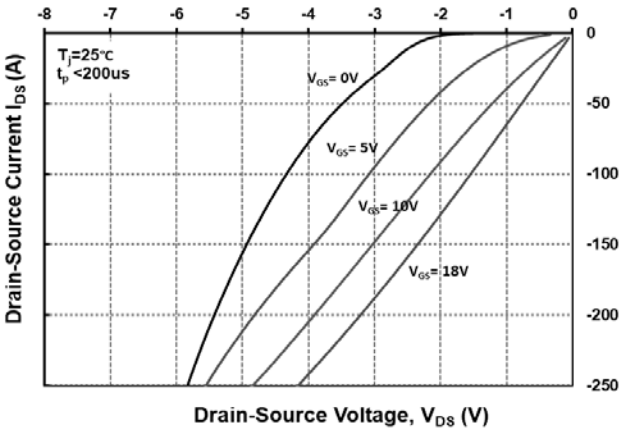


Fig 15. 3rd Quadrant Characteristics @ 175°C

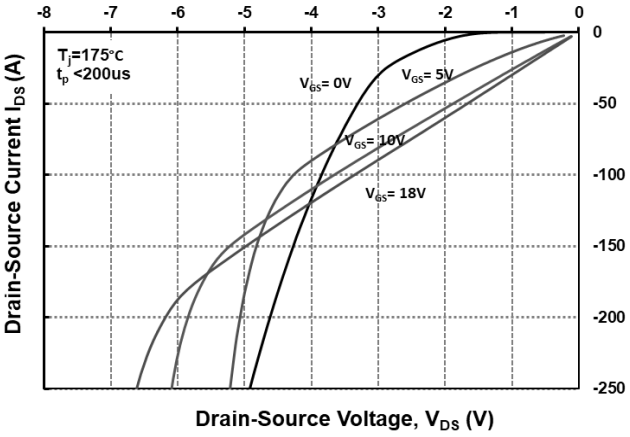


Fig 16. Output Capacitor Stored Energy

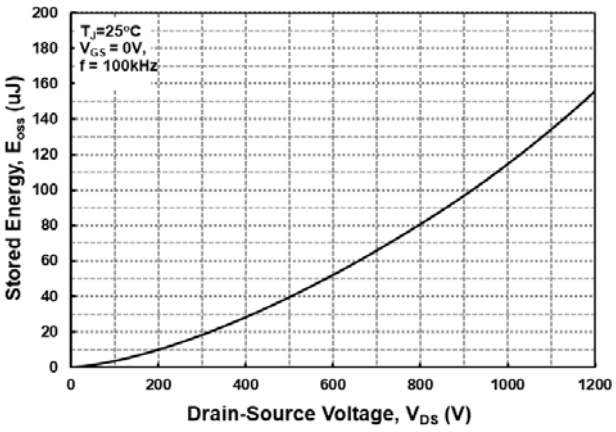


Fig 17. Capacitances vs. Drain-Source Voltage (0-200V)

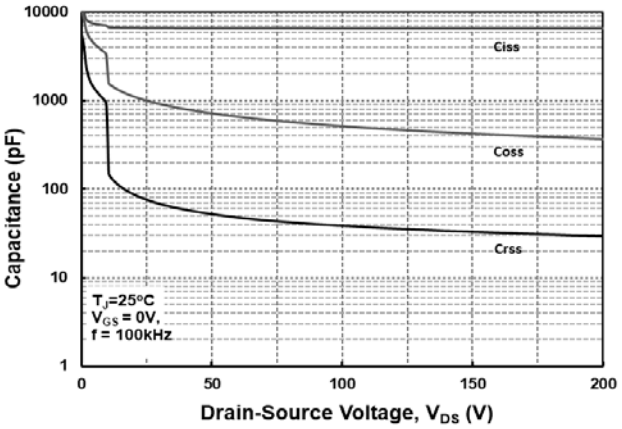
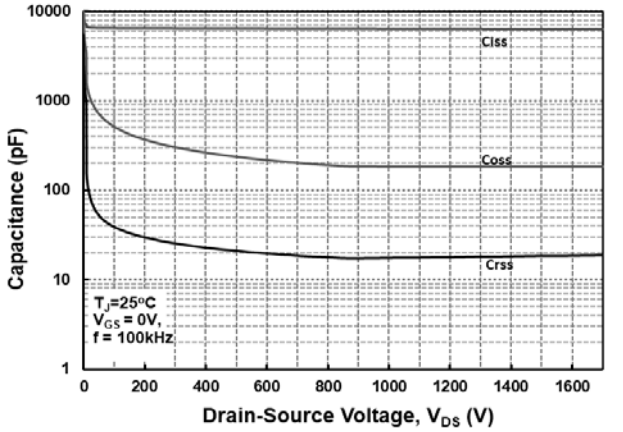


Fig 18. Capacitances vs. Drain-Source Voltage (0-1700V)



Typical Performance

Fig 19. Continuous Drain Current Derating vs. Case Temperature

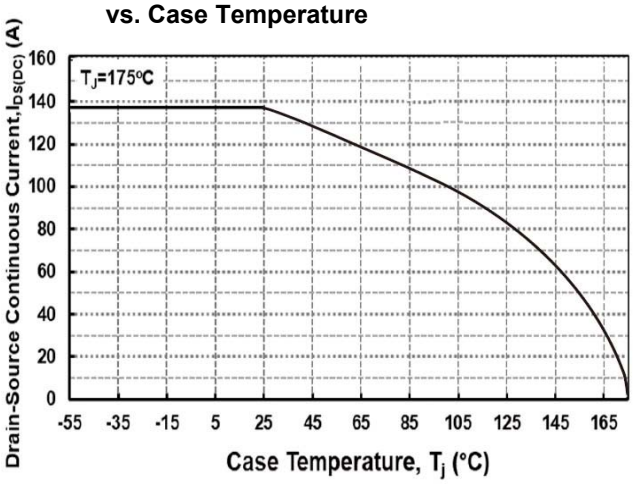


Fig 20. Maximum Power Dissipation Derating vs. Case Temperature

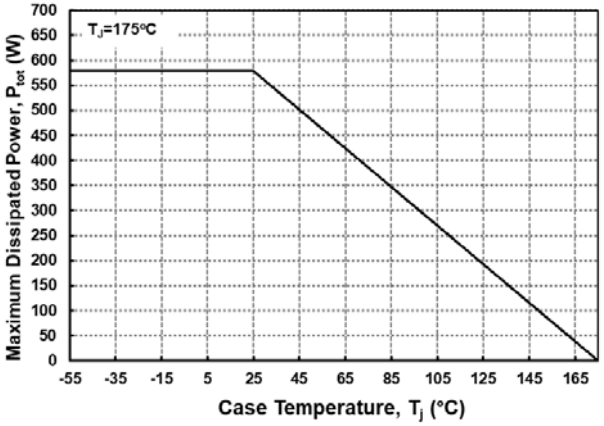


Fig 21. Transient Thermal Impedance (Junction-Case)

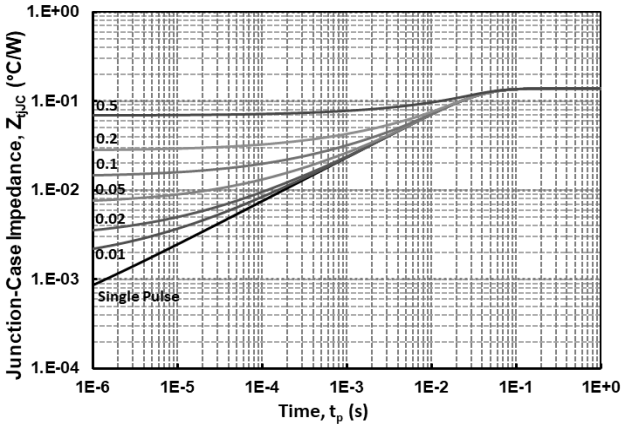


Fig 22. Safe Operating Area

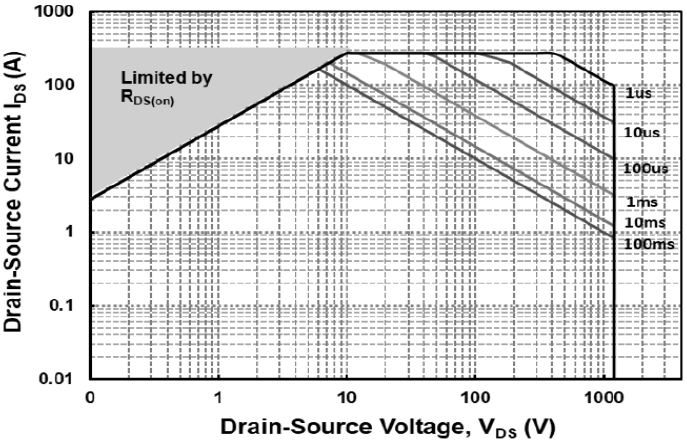


Fig 23. Clamped Inductive Switching Energy vs Drain Current (V_{DD} = 850V)

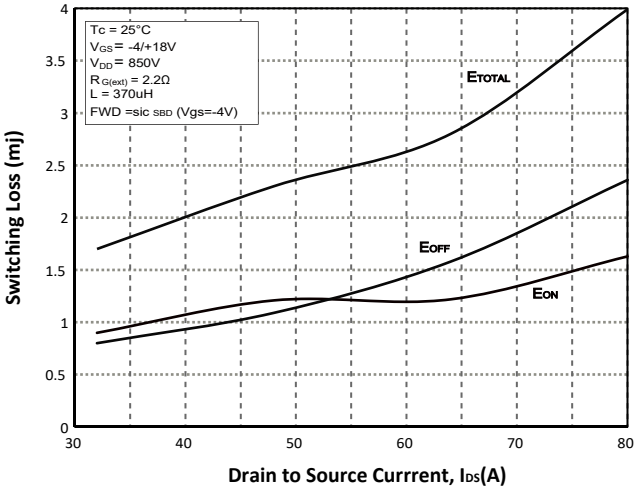
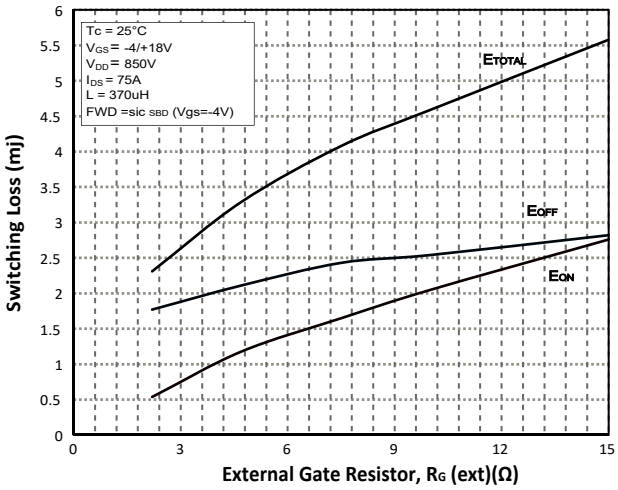


Fig 24. Clamped Inductive Switching Energy vs External Gate Resistor $R_{G(ext)}$



Typical Performance

Fig 25. Switching Times vs Drain Current (V_{DD} = 850V)

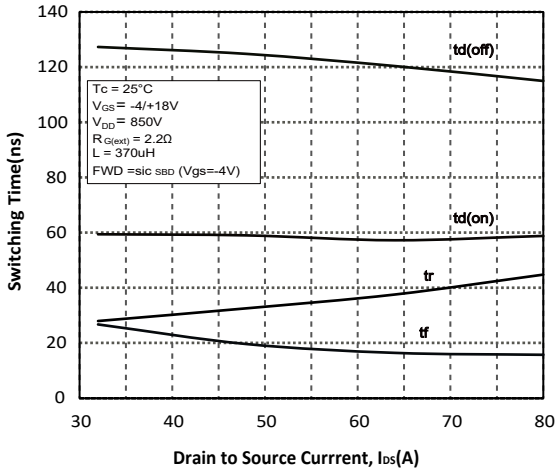
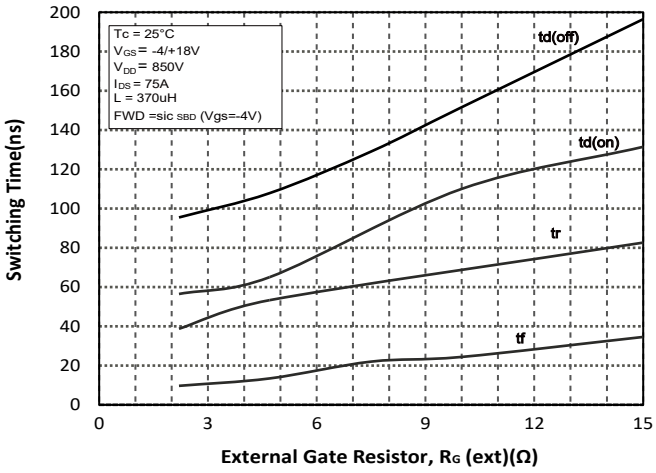


Fig 26. Switching Times vs External Gate Resistor $R_{G(ext)}$



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